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### IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Appln. Ser. No.:	Filed:	Inventor(s):	Atty Dkt:
09/826,383	4 April 2001	S. Yoshida	114Gl-143 (0694-143)
Title: Electromagi Same, and	netic Noise Suppre Method of Manufac	essor, Semiconducto cturing the Same	r Device Using the
Examiner: Khiem Nguyen		Art Unit: 2823	

Asst. Comm'r for Patents Washington, D.C. 20231-0001

VIA FACSIMILE 703-872-9306

#### REPLY

Dear Sir:

This Reply is made in complete and timely response to the Office action mailed 8 July 2004, in which the sole rejection was under 35 U.S.C. 102(e) over Awakura (et al.), U.S. Pat. No 6,654,573. The rejection is traversed because the Awakura reference is not available as prior art against the instant application. Awakura is based on an application filed (§102(e) date) 3 April 2001. The instant application claims priority from four prior-filed Japanese applications, all of which were filed in 2000. Because the instant application has an earlier effective filing date, Awakura is not available as a reference under §102, and so the instant rejections should be withdrawn.

# CERTIFICATE OF MAILING OR TRANSMISSION - 37 CFR 1.8

I hereby certify that I have a reasonable basis that this paper, along with any referred to above, (i) are being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to Commissionor for Patents, PO Box 1450, Alexandria, VA 22313-1450, or (ii) are being transmitted to the U.S. Patent & Trademark Office in accordance with 37 CFR § 1.6(d)

NAME Brad Ruben
SIGNATURE Brad F

09/826,383

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Respectfully submitted,

Bradley N. Ruben, Reg. No. 32,058

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4 October 2004

114GI-142

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